

FIG. 3

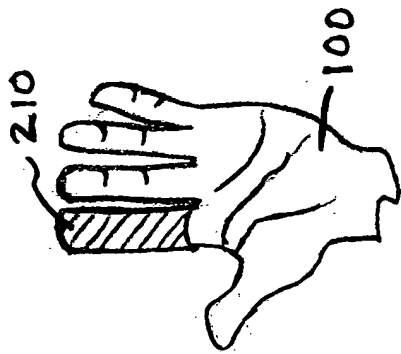


FIG. 4

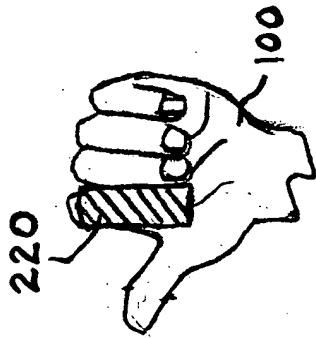


FIG. 5

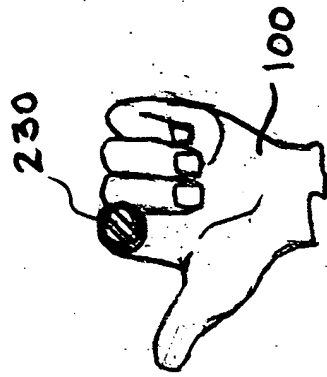


FIG. 6

Map of circuitry for computer input device

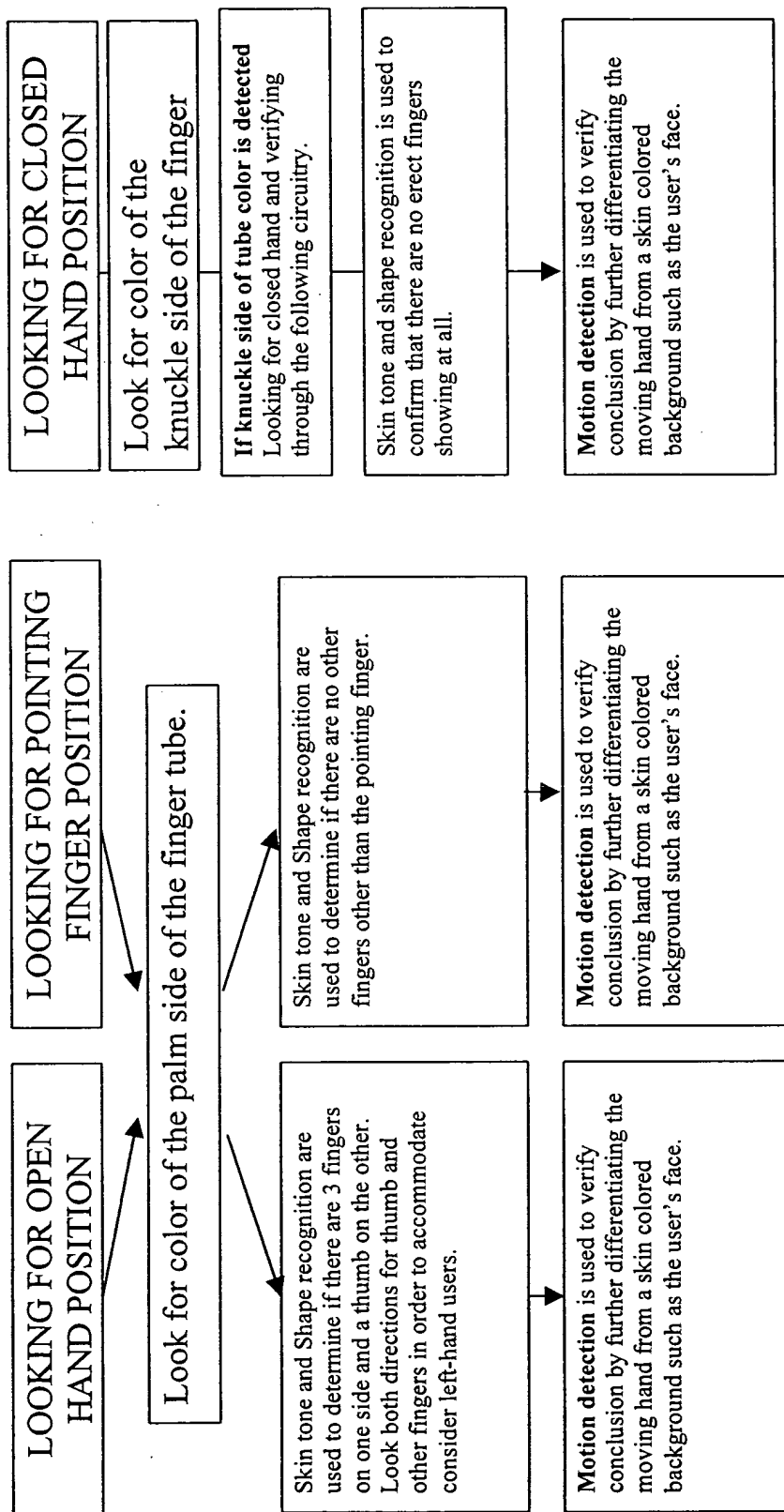


FIG. 7

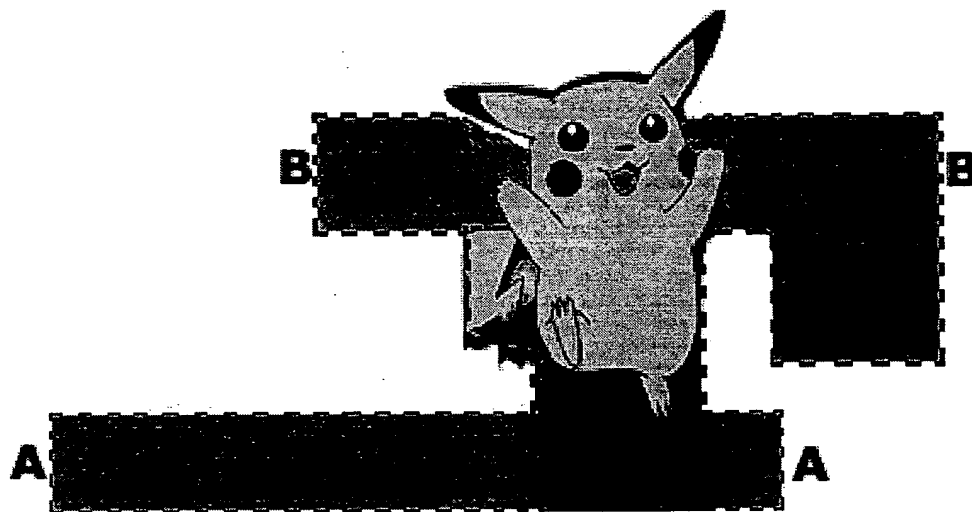


FIG. 8

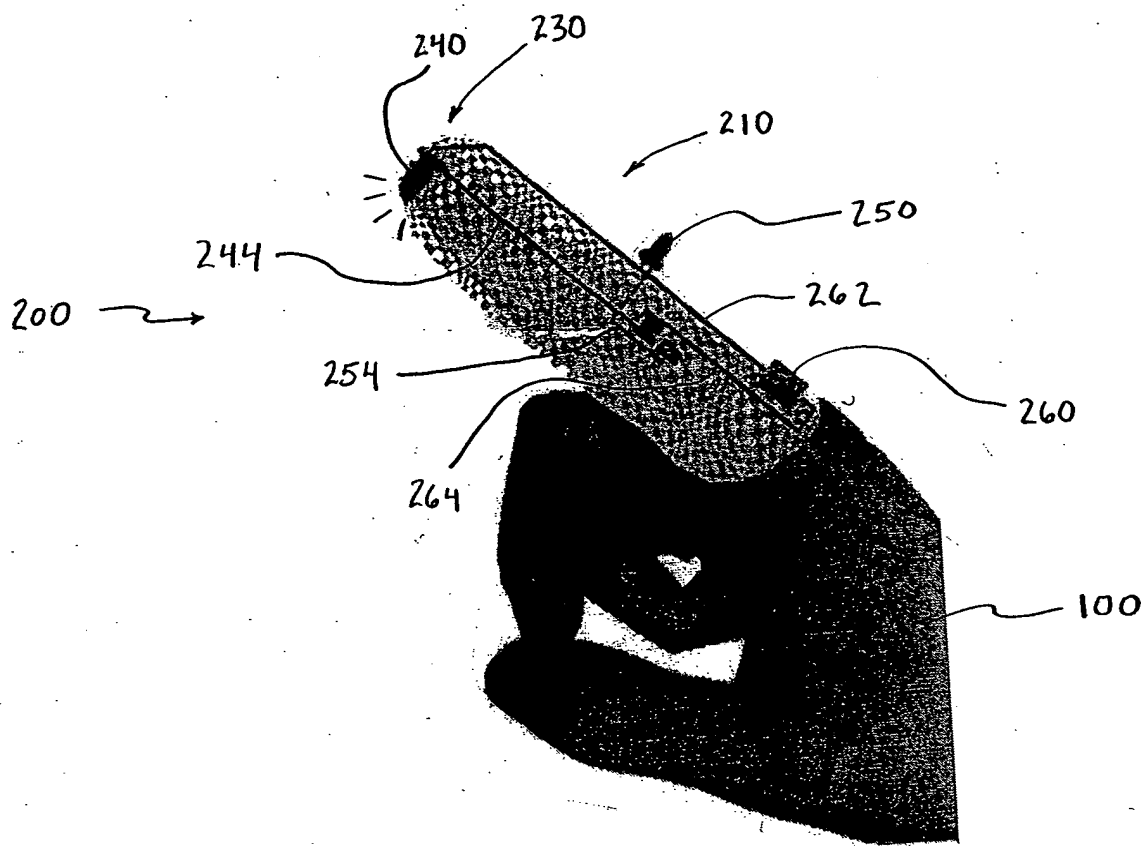


FIG. 9

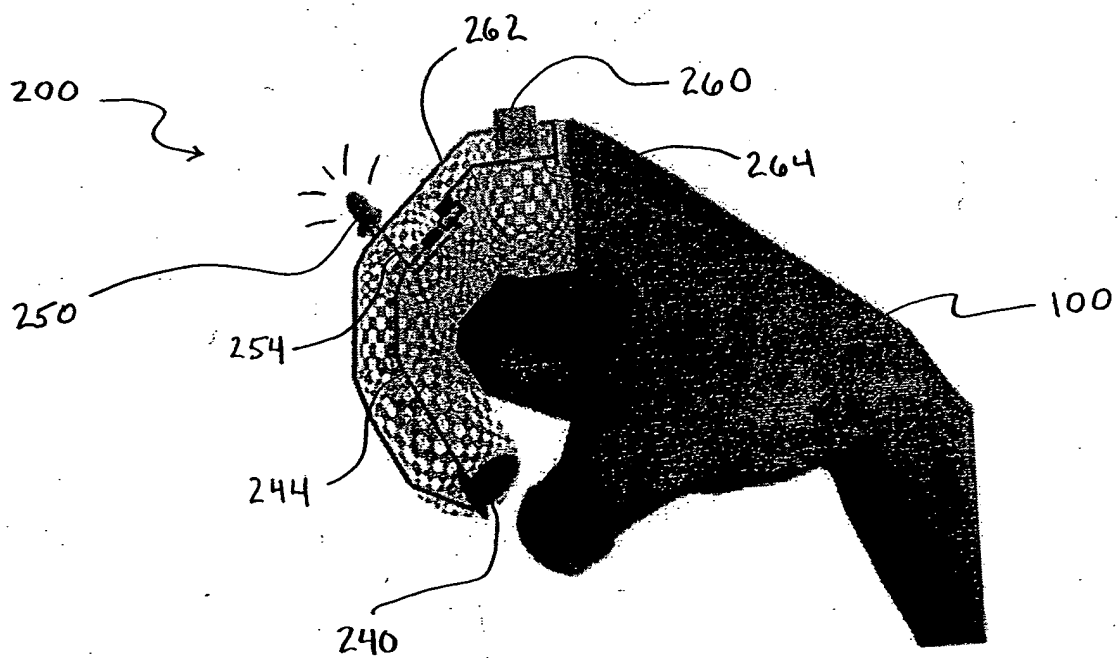


FIG. 10

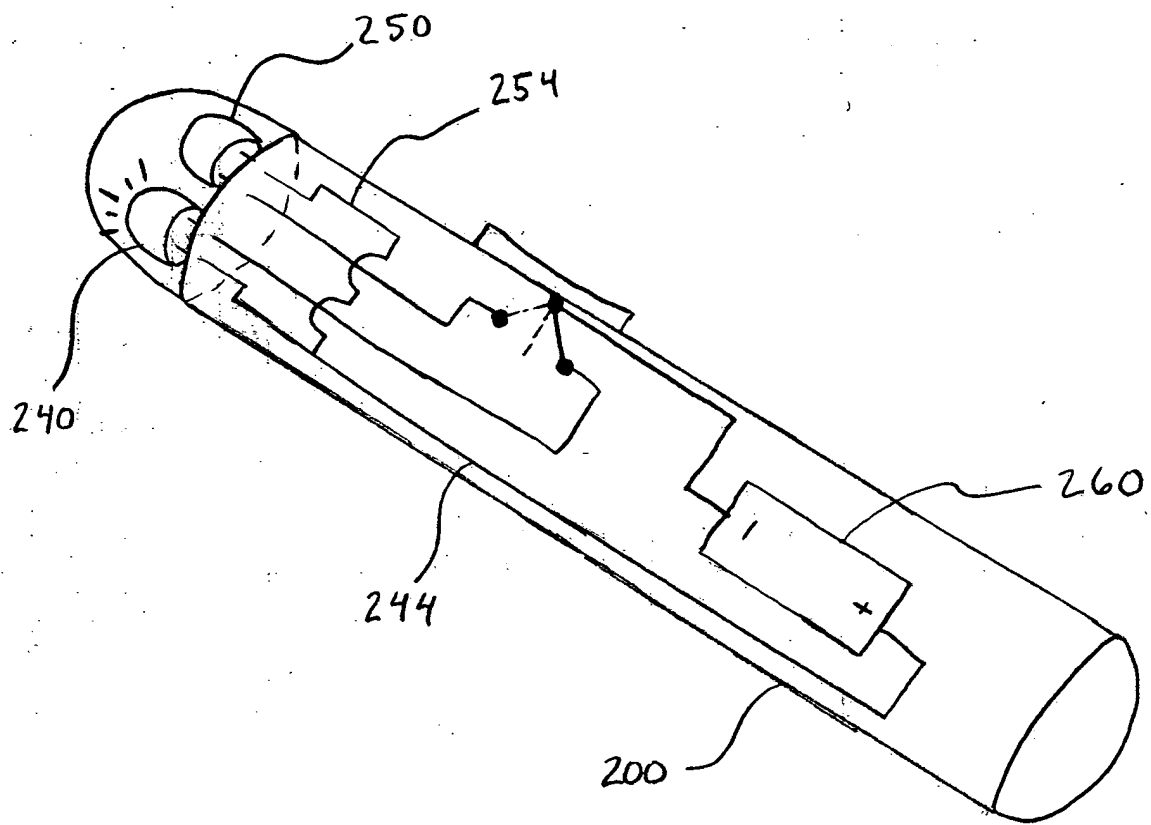


FIG. 11